

Applicant : Kazutaka Inukai  
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Amendments to the Specification:

Please replace the paragraph beginning at page 14, line 11 as with the following amended paragraph:

For a case of performing display of  $2^{n-1}$  gradations, the pixels are placed in an on state in the display periods  $Tr1$  to  $Tr(n-1)$ , and the pixels are placed in an off state during the display period  $Trn$ . Further, the pixels are placed in a turned off state during the display periods  $Tr1$  to  $Tr(n-1)$ , and are turned on during the display period  $Trn$ , when performing display of  $[[2^{n+1} + 1]]$   $2^{n-1} + 1$  gradations.